SPGAMT Products Pvt Ltd

*** Today's problems:**

* Big data work now needs much power
* Common CPU and GPU solutions cause delay and waste
* Wide usage brings strain on power

*** SPGAMT Solution:**

* Guidance is taken from Sanskrit grammar & Mimansa
* Parallel data paths are kept precise & concise for better performance & efficient power usage
* Competitors : Nvidia, Intel, AMD
* Examined & encouraged at national level by DST & NQM Director
* Lead by expert with 27 US patents

Work Done & Gentle Plan

* **Present effort for low hanging fruit:**

    * First work is for FPGA based solution for specific deep tasks.

    * Step by step move is planned with care.

*** Way ahead:**

    * Early years: FPGA proof and steady build.

    * Middle years: ASIC plan and design.

    * Later stage: First silicon and field use.

* **Details:**

    * Technical: Under Disclosures section of www.spgamt.com

    * Funding ask: Accompanying document